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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/847.963	05/02/2001	Jason Seung-Min Kim	2100653.991380	7268
27498	7590	11/08/2006	EXAMINER	
PILLSBURY WINTHROP SHAW PITTMAN LLP			HSU, ALPUS	
P.O. BOX 10500			ART UNIT	
MCLEAN, VA 22102			PAPER NUMBER	

2616

DATE MAILED: 11/08/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/847,963

Applicant(s)

KIM, JASON SEUNG-MIN

Examiner

Alpus H. Hsu

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 September 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-34 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8, 11-22, 25-31 and 34 is/are rejected.
- 7) ☒ Claim(s) 9, 10, 23, 24, 32 and 33 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

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1. Applicant's arguments with respect to claims 1-34 have been considered but are moot in view of the new ground(s) of rejection.
2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1, 3, 7, 8, 11, 16, 18, 21, 22, 25-29, and 34 are rejected under 35 U.S.C. 102(e) as being anticipated by DIGIORGIO et al. in U.S. Patent No. 6,286,060 B1, hereinafter referred as DIGIORGIO.

Referring to claims 1, 3, 7, 8, 11, 16, 18, 21, 22, 25-28, DIGIORGIO discloses an electronic device (Host Computing Device) having a configurable port (406 and 410-413) for connecting with a variety of interface device types (415-417), comprising a register set (418) or means for storing one or more sets of configuration data providing each set of configuration data corresponding to a particular interface type that may be connected to the electronic device ; and a multiplexer (405) or means for conforming a data signal in accordance with the configuration data associated with a particular interface device type connected to the electronic device (see col. 3, line 65 to col. 4, line 21, col. 6, line 24 to col. 7, line 21, col. 8, line 49 to col. 10, line 28).

Referring to claims 29 and 34, DIGIORGIO discloses a method for configuring a port connected with a variety of interface device types, by storing one or more sets of configuration data, providing each set of configuration data corresponding to a particular interface type that

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may be connected to an electronic device ; conforming a data signal in accordance with the configuration data associated with a particular interface device type connected to the electronic device, in response to enabling the port to transmit data to the connected device, conforming an output data signal in accordance with the configuration data associated with the particular interface type connected to the electronic device; and , in response to enabling the port to receive data from the connected device, receiving an input data signal and translating the input data signal as appropriate data signal to interface with the port (see col. 3, line 65 to col. 4, line 21, col. 6, line 24 to col. 7, line 21, col. 8, line 49 to col. 10, line 28).

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 2, 17 and 30 are rejected under 35 U.S.C. 103(a) as being unpatentable over DIGIORGIO in view of SHIMURA (of record).

Referring to claims 2, 17 and 30, DIGIORGIO differs from the claims, in that, it fails to disclose the specific interface device types including open drain interface type, open collector interface type, totem pole interface type, and tri-state buffer interface type, which is well known in the art and commonly used in communications network for appropriate connection with external devices. SHIMURA, for example, from the similar field of endeavor, teaches the uses of specific interface device types including open drain interface type, open collector interface type, totem pole interface type, and tri-state buffer interface type (see abstract), which can be

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easily adopted by one of ordinary skill in the art into the device or method of DIGIORGIO to further improve the system capability for flexibility and adaptability.

6. Claims 4-6, 12-15, 19, 20, and 31 are rejected under 35 U.S.C. 103(a) as being unpatentable over DIGIORGIO in view of HIGUCHI (of record).

Referring to claims 4-6, 12-15, 19, 20, and 31, DIGIORGIO differs from the claims, in that, it fails to disclose the specific programmable registers being D-type flip-flops for configuring and enabling signaling for the configurable port of the electronic device, and controlling input and output signaling for the configurable port, which as also well known in the art and commonly used in programmable logic device for signaling and control purposes. HIGUCHI, for example, from the similar field of endeavor, teaches the uses of specific programmable registers being D-type flip-flops for configuring and enabling signaling for the configurable port of the electronic device, and controlling input and output signaling for the configurable port (see col. 4, lines 22-29), which can be easily adopted by one of ordinary skill in the art to implement into the device or method of DIGIORGIO to further improve the system controllability.

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Robinson et al. '788 & '625 and Uppunda et al. are additionally cited to show the common feature of programmable input/output (I/O) controller utilizing memory and configurable port for coupling multiple peripheral devices similar to the claimed invention.

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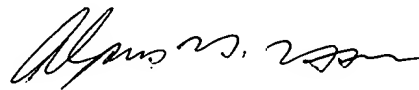
8. Claims 9, 10, 23, 24, 32 and 33 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alpus H. Hsu whose telephone number is (571)272-3146. The examiner can normally be reached on M-F (5:30-3:00) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wellington Chin can be reached on (571)272-3134. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

AHH



Alpus H. Hsu
Primary Examiner
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